

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: Phadnis et al

Appl. No.: 09/976,004

Filed: October 15, 2001

For: Reducing Overhead When Setting  
Up Multiple Virtual Circuits Using  
Signaling Protocols

Art Unit: 2616

Examiner: Robert W. Wilson

Atty. Docket: CSCO-010/4390

**Amendment and Response Under 37 C.F.R. § 1.114**

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Final Office Action mailed 08/21/2007 and further to the telephone conversation with Examiner Wilson on 18 October 2007, Applicants submit the following amendments and remarks accompanying the Request for Continued Examination (RCE).

**Amendments to the Specification** begin on page 2 of this paper.

**Amendments to the claims** are reflected in the listing of claims which begin on page 3 of this paper.

**Remarks** begin at page number 21 of this paper.

It is not believed that extensions of time or fees for net addition of claims are required, beyond those which may otherwise be provided for in documents accompanying this paper. However, in the event that additional extensions of time are necessary to allow consideration of this paper, then such extensions of time are hereby petitioned under 37 C.F.R. § 1.136(a), and any fees required therefor (including fees for net addition of claims) are hereby authorized to be charged to Deposit Account No.: 20-0674.